| Table 9‑5 – Association of ctxIdxOffset and syntax elements for each initializationType in the initialization process | | | | | | |
| --- | --- | --- | --- | --- | --- | --- |
| **Syntax structure** | **Syntax element** | **ctxTable** | **initType** | | |
| **0** | **1** | **2** |
| coding\_unit( ) | cu\_skip\_flag[ ][ ] |  |  | 0 | 3 |
| pred\_mode\_ibc\_flag |  |  |  |  |
| pred\_mode\_flag |  |  |  |  |
| intra\_luma\_ref\_idx[ ][ ] |  |  |  |  |
| intra\_subpartitions\_mode\_flag |  |  |  |  |
| intra\_subpartitions\_split\_flag |  |  |  |  |
| intra\_luma\_mpm\_flag[ ][ ] |  |  |  |  |
| intra\_chroma\_pred\_mode[ ][ ] |  |  |  |  |
| general\_merge\_flag[ ][ ] |  |  |  |  |
| inter\_pred\_idc[ x0 ][ y0 ] |  |  |  |  |
| inter\_affine\_flag[ ][ ] |  |  |  |  |
| cu\_affine\_type\_flag[ ][ ] |  |  |  |  |
| sym\_mvd\_flag[ ][ ] |  |  |  |  |
| ref\_idx\_l0[ ][ ] |  |  |  |  |
| mvp\_l0\_flag[ ][ ] |  |  |  |  |
| ref\_idx\_l1[ ][ ] |  |  |  |  |
| mvp\_l1\_flag[ ][ ] |  |  |  |  |
| avmr\_flag[ ][ ] |  |  |  |  |
| amvr\_precision\_flag[ ][ ] |  |  |  |  |
| bcw\_idx[ ][ ] |  |  |  |  |
| cu\_cbf |  |  |  |  |
| cu\_sbt\_flag |  |  |  |  |
| cu\_sbt\_quad\_flag |  |  |  |  |
| cu\_sbt\_horizontal\_flag |  |  |  |  |
| cu\_sbt\_pos\_flag |  |  |  |  |

| Table 9‑9 – Syntax elements and associated binarizations | | | |
| --- | --- | --- | --- |
| **Syntax structure** | **Syntax element** | **Binarization** | |
| **Process** | **Input parameters** |
| coding\_unit( ) | cu\_skip\_flag[ ][ ] | FL | cMax = 1 |
| pred\_mode\_ibc\_flag | FL | cMax = 1 |
| pred\_mode\_flag | FL | cMax = 1 |
| pcm\_flag[ ][ ] | FL | cMax = 1 |
| intra\_luma\_ref\_idx[ ][ ] | TR | cMax = 2, cRiceParam = 0 |
| intra\_subpartitions\_mode\_flag | FL | cMax = 1 |
| intra\_subpartitions\_split\_flag | FL | cMax = 1 |
| intra\_luma\_mpm\_flag[ ][ ] | FL | cMax = 1 |
| intra\_luma\_mpm\_idx[ ][ ] | TR | cMax = 5, cRiceParam = 0 |
| intra\_luma\_mpm\_remainder[ ][ ] | TB | cMax = 60 |
| intra\_chroma\_pred\_mode[ ][ ] | 9.5.3.8 | - |
| general\_merge\_flag[ ][ ] | FL | cMax = 1 |
| inter\_pred\_idc[ x0 ][ y0 ] | 9.5.3.9 | cbWidth, cbHeight |
| inter\_affine\_flag[ ][ ] | FL | cMax = 1 |
| cu\_affine\_type\_flag[ ][ ] | FL | cMax = 1 |
| sym\_mvd\_flag[ ][ ] | FL | cMax = 1 |
| ref\_idx\_l0[ ][ ] | TR | cMax = NumRefIdxActive[ 0 ] − 1, cRiceParam = 0 |
| mvp\_l0\_flag[ ][ ] | FL | cMax = 1 |
| ref\_idx\_l1[ ][ ] | TR | cMax = NumRefIdxActive[ 1 ] − 1, cRiceParam = 0 |
| mvp\_l1\_flag[ ][ ] | FL | cMax = 1 |
| avmr\_flag[ ][ ] | FL | cMax = 1 |
| amvr\_precision\_flag[ ][ ] | FL | cMax = 1 |
| bcw\_idx[ ][ ] | TR | cMax = NoBackwardPredFlag ? 4: 2 |
| cu\_cbf | FL | cMax = 1 |
| cu\_sbt\_flag | FL | cMax = 1 |
| cu\_sbt\_quad\_flag | FL | cMax = 1 |
| cu\_sbt\_horizontal\_flag | FL | cMax = 1 |
| cu\_sbt\_pos\_flag | FL | cMax = 1 |



| Table 9‑15 – Assignment of ctxInc to syntax elements with context coded bins | | | | | | |
| --- | --- | --- | --- | --- | --- | --- |
| **Syntax element** | **binIdx** | | | | | |
| **0** | **1** | **2** | **3** | **4** | **>= 5** |
| … |  |  |  |  |  |  |
| inter\_affine\_flag[ ][ ] | 0,1,2 (clause 9.5.4.2.2) | na | na | na | na | na |
| cu\_affine\_type\_flag[ ][ ] | 0 | na | na | na | na | na |
| sym\_mvd\_flag[ ][ ] | 0 | na | na | na | na | na |
| ref\_idx\_l0[ ][ ] | 0 | 1 | bypass | bypass | bypass | bypass |
| ref\_idx\_l1[ ][ ] | 0 | 1 | bypass | bypass | bypass | bypass |
| … |  |  |  |  |  |  |